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(54) Title: METHOD AND DEVICE FOR PARTITIONING LARGE COMPUTER PROGRAMS

(57) Abstract: A method for partitioning large computer programs and or algorithms at least part of which is to be executed by an array of reconfigurable units such as ALUS, comprising the steps of defining a maximum allowable size to be mapped onto the array, partitioning the program such that its separate parts minimize the overall execution time and providing a mapping onto the array not exceeding the maximum allowable size is described.

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A. CLASSIFICATION OF SUBJECT MATTER
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B. FIELDS SEARCHED
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IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)
EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	BAUMGARTE V ET AL: "PACT XPP - A Self-Reconfigurable Data Processing Architecture" XP002256066 Retrieved from the Internet: <URL: ftp://ftp.pactcorp.com/info/publications/rsa01.pdf> 'retrieved on 2003-09-29! the whole document	1,2
X	JANTSCH A ET AL: "A case study on hardware/software partitioning" FPGAS FOR CUSTOM COMPUTING MACHINES, 1994. PROCEEDINGS. IEEE WORKSHOP ON NAPA VALLEY, CA, USA 10-13 APRIL 1994, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, 10 April 1994 (1994-04-10), pages 111-118, XP010098092 ISBN: 0-8186-5490-2 the whole document	1,2

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☐ Patent family members are listed in annex.

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		Relevant to claim No.
Category *	Citation of document, with indication, where appropriate, of the relevant passages	
X	<p>GOKHALE M B ET AL: "Automatic allocation of arrays to memories in FPGA processors with multiple memory banks" FIELD-PROGRAMMABLE CUSTOM COMPUTING MACHINES, 1999. FCCM '99. PROCEEDINGS. SEVENTH ANNUAL IEEE SYMPOSIUM ON NAPA VALLEY, CA, USA 21-23 APRIL 1999, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 21 April 1999 (1999-04-21), pages 63-69, XP010359163 ISBN: 0-7695-0375-6 the whole document</p>	1,2
X	<p>HAMMES J ET AL: "CAMERON: HIGH LEVEL LANGUAGE COMPILATION FOR RECONFIGURABLE SYSTEM", PARALLEL ARCHITECTURES AND COMPILATION TECHNIQUES, 1999. PROCEEDINGS. 1999 INTERNATIONAL CONFERENCE ON NEWPORT BEACH, CA, USA 12-16 OCT. 1999, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, PAGE(S) 1-9 XP002256349 ISBN: 0-7695-0425-6 the whole document</p>	1,2
X	<p>DINIZ P ET AL: "Automatic synthesis of data storage and control structures for FPGA-based computing engines" FIELD-PROGRAMMABLE CUSTOM COMPUTING MACHINES, 2000 IEEE SYMPOSIUM ON NAPA VALLEY, CA, USA 17-19 APRIL 2000, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 17 April 2000 (2000-04-17), pages 91-100, XP010531928 ISBN: 0-7695-0871-5 the whole document</p>	1,2
A	<p>WEINHARDT M ET AL: "Pipeline vectorization for reconfigurable systems" FIELD-PROGRAMMABLE CUSTOM COMPUTING MACHINES, 1999. FCCM '99. PROCEEDINGS. SEVENTH ANNUAL IEEE SYMPOSIUM ON NAPA VALLEY, CA, USA 21-23 APRIL 1999, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 21 April 1999 (1999-04-21), pages 52-62, XP010359172 ISBN: 0-7695-0375-6 cited in the application the whole document</p>	1,2

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INTERNATIONAL SEARCH REPORT

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category * Citation of document, with indication, where appropriate, of the relevant passages

Relevant to claim No.

A

SIEMERS C: "RECHENFABRIK ANSAETZE FUER
EXTREM PARALLELE PROZESSOREN"
CT MAGAZIN FUER COMPUTER TECHNIK, VERLAG
HEINZ HEISE GMBH., HANNOVER, DE,
no. 15, 16 July 2001 (2001-07-16), pages
170-179, XP001108091
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